

# QSFP+-SFP+-DAC-3m 40G QSFP+ to 10G SFP+ Direct Attach Copper Cables



## Product Features

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- Single power supply +3.3V;
- Hot-pluggable electrical interface;
- Low insertion loss, crosstalk ;
- 4 duplex lanes active optical cable at 10Gbps;
- SFF-8436 QSFP+ compliant;
- SFF-8431 SFP+ compliant;
- RoHS compliant;
- All-metal housing for superior EMI performance;
- Operating Case Temperature: 0°C~+70°C;
- Storage Temperature: -40°C~+85°C;

## Product Description

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LR-LINK QSFP+-SFP+-DAC-3m is the preferred solution for 10G speed short-range data transmission with low power consumption, good stability and high cost performance. The QSFP+ to SFP+ passive cable is used for data transfer between a 40G QSFP+ one port and one 10G SFP+ ports, providing a low-cost solution for data transfer services within and between data center racks. This product complies with the SFF-8436, QSFP+ MSA and IEEE 802.3ba standards.

QSFP+-SFP+-DAC-3m is the preferred solution for short-distance data transmission at 10G rate. It complies with SFF-8436, QSFP MSA and IEEE 802.3ae standards, and is suitable for data transmission within and between racks in data centers.

## Technical parameter

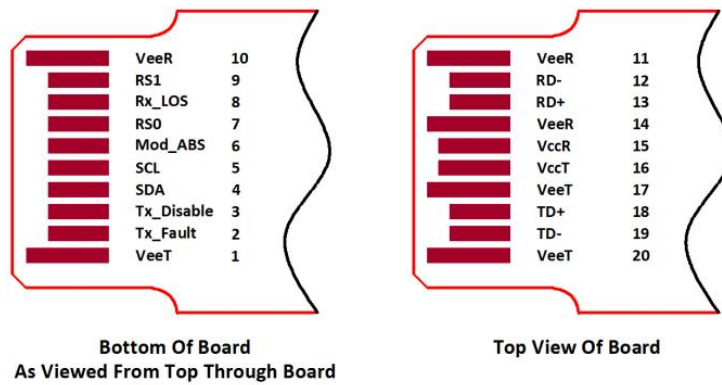
Absolute Maximum Ratings					
Parameter	Symbol	Min.	Typical	Max.	Unit
Storage Temperature	T <sub>ST</sub>	-40	-	85	°C
Operating temperature	T <sub>OPC</sub>	0	-	70	°C

## Order Information

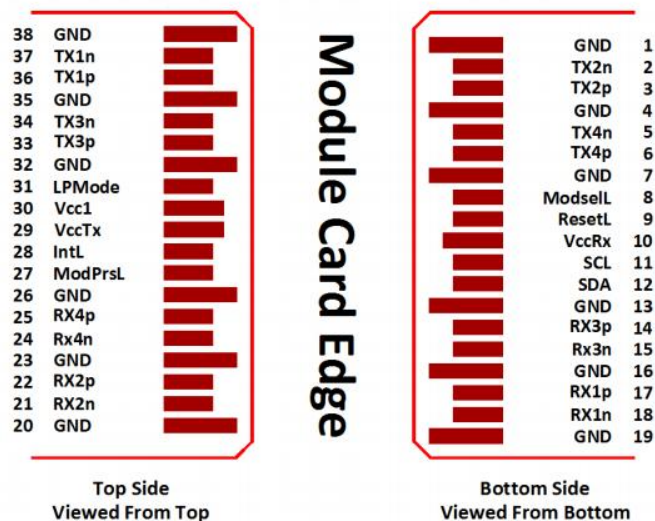
P/N	Description	Length
QSFP+-SFP+-DAC-3m	40G QSFP+ to 10G SFP+ Direct Attach Copper Cables	3m

PS: Above details are only for reference, if there is any change, no prior notice.

## SFP+ Module Pad Layout



## QSFP+ Module Pad Layout



## SFP+ Pin Definitions

Pin	Name	Function/Description
1	VeeT	Transmitter Ground
2	TX_Fault	N/A
3	TX_Disable	Transmitter Disable
4	SDA	Two Wire Serial Interface Data Line
5	SCL	Two Wire Serial Interface Clock
6	Mod_ABS	Module Absent, connected to VeeT or VeeR
7	RS0	N/A
8	Rx_LOS	Receiver Loss of Signal Indication
9	RS1	N/A
10	VeeR	Receiver Ground
11	VeeR	Receiver Ground
12	RD-	Receiver Inverted Data Output
13	RD+	Receiver Non-Inverted Data Output
14	VeeR	Receiver Ground
15	VccR	Receiver 3.3 V Supply
16	VccT	Transmitter 3.3 V Supply
17	VeeT	Transmitter Ground
18	TD+	Transmitter Non-Inverted Data Input
19	TD-	Transmitter Inverted Data Input
20	VeeT	Transmitter Ground

## QSFP+ Pin Definitions

Pin	Name	Function/Description
1	GND	Ground
2	Tx2n	Transmitter Inverted Data Input
3	Tx2p	Transmitter Non-Inverted Data Input
4	GND	Ground
5	Tx4n	Transmitter Inverted Data Input
6	Tx4p	Transmitter Non-Inverted Data Input
7	GND	Ground
8	ModSelL	Module Select
9	ResetL	Module Reset
10	Vcc Rx	+3.3V Power Supply Receiver
11	SCL	2-wire serial interface clock
12	SDA	2-wire serial interface data
13	GND	Ground
14	Rx3p	Receiver Non-Inverted Data Output
15	Rx3n	Receiver Inverted Data Output
16	GND	Ground
17	Rx1p	Receiver Non-Inverted Data Output
18	Rx1n	Receiver Inverted Data Output
19	GND	Ground
20	GND	Ground
21	Rx2n	Receiver Inverted Data Output
22	Rx2p	Receiver Non-Inverted Data Output
23	GND	Ground

24	Rx4n	Receiver Inverted Data Output
25	Rx4p	Receiver Non-Inverted Data Output
26	GND	Ground
27	ModPrsL	Module Present
28	IntL	Interrupt
29	Vcc Tx	+3.3V Power supply transmitter
30	Vcc1	+3.3V Power supply
31	LPMode	Low Power Mode
32	GND	Ground
33	Tx3p	Transmitter Non-Inverted Data Input
34	Tx3n	Transmitter Inverted Data Input
35	GND	Ground
36	Tx1p	Transmitter Non-Inverted Data Input
37	Tx1n	Transmitter Inverted Data Input
38	GND	Ground

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